ABSTRACT OF THE DISCLOSURE

The present invention relates to an error control method for a channel equalizer, which includes the steps of: multiplying a first error calculated from a DD slicer and a second error calculated from a Sato slicer each by a scale constant; taking the absolute value of the real part and imaginary part of the first error calculated from the DD slicer, summing them, and then obtaining the absolute value of the first error; obtaining the absolute value of an inverse response signal of a channel by multiplying the absolute value of the first error by the second error multiplied by the scale constant and adding the resultant value to a first error multiplied by the scale constant; and generating a filter tab coefficient to reproduce a signal transmitted from a sending end by feeding back the absolute value of the inverse response of the channel signal to the equalizer filter. Even if the structure of the DD error size calculation unit is modified in order to obtain the absolute value of the first error, the number of gates is reduced, error update speed is improved, and the size is made smaller while maintaining the performance of the G-pseudo channel equalizer in the conventional art, whereby the performance of the receiver is improved.